

Model Name: GA-H81M-DS2

Revision
3.0

SHEET TITLE

01	COVER SHEET
02	BOM & PCB MODIFY HISTORY
03	BLOCK DIAGRAM
04	CPU_LGA1150-A
05	CPU_LGA1150-B
06	CPU_LGA1150-C
07	DDR III CHANNEL A
08	DDR III CHANNEL B
09	PCH_FDI,DMI,USB,PCIE,NVRAM
10	PCH_DP,CLK BUFFER
11	PCH_HOST,SATA,PCI
12	PCH_GPIO,CTRL,AUDIO
13	PCH_PWR,GND
14	PCI EXPRESS*16 SLOT
15	PCI EXPRESS X1 *2 SLOT
16	ITE 8620 LPC IO
17	COM,KB_MS_USB,USB30_20
18	HWM,FAN CTRL,OV,-PROCHOT
19	DUAL BIOS
20	FP,FUSB,SPK,SATALED
21	Realtek ALC887-VD2
22	REAR AUDIO JACK
23	REALTEK RTL8111G
24	DISCRETE POWER
25	ATX , CLOCK GEN
26	VCORE ISL95812_1
27	VCORE ISL95812_2

SHEET TITLE

28	RT8120_DDR POWER
29	LPT

Gigabyte Technology			
Title			
Cover Sheet			
Size Custom	Document Number	GA-H81M-DS2	Rev 3.0
Date:	Tuesday, August 19, 2014	Sheet	1 of 29

Revision
3.0

Component value change history

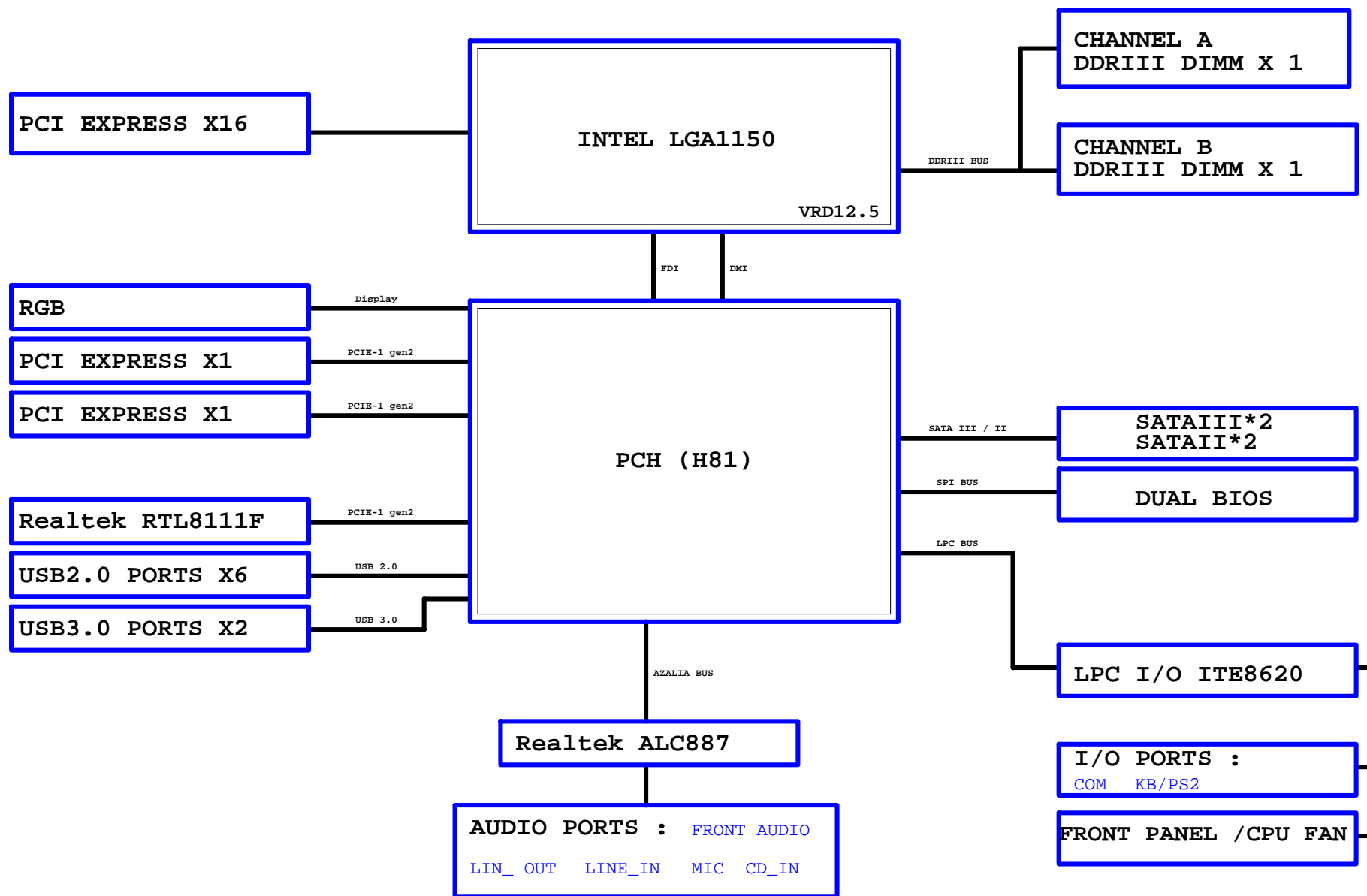
2013/06/27

[illegible]

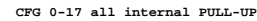
Circuit or PCB layout change

[illegible]

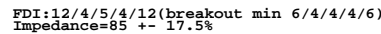
BLOCK DIAGRAM



(E)

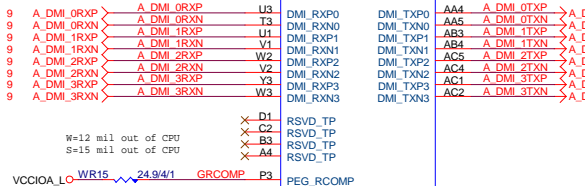


(D)

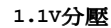


(c)

Impedance=80 +- 17.5%



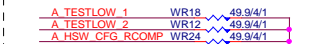
Impedance=85 +- 17.5%



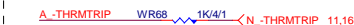
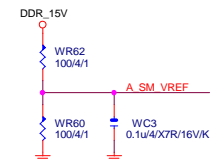
CPU SVID



CPU	PU/PD
-----	-------



SM	REF
1	1
2	2
3	3
4	4
5	5
6	6
7	7
8	8
9	9
10	10
11	11
12	12
13	13
14	14
15	15
16	16
17	17
18	18
19	19
20	20
21	21
22	22
23	23
24	24
25	25
26	26
27	27
28	28
29	29
30	30
31	31
32	32
33	33
34	34
35	35
36	36
37	37
38	38
39	39
40	40
41	41
42	42
43	43
44	44
45	45
46	46
47	47
48	48
49	49
50	50
51	51
52	52
53	53
54	54
55	55
56	56
57	57
58	58
59	59
60	60
61	61
62	62
63	63
64	64
65	65
66	66
67	67
68	68
69	69
70	70
71	71
72	72
73	73
74	74
75	75
76	76
77	77
78	78
79	79
80	80
81	81
82	82
83	83
84	84
85	85
86	86
87	87
88	88
89	89
90	90
91	91
92	92
93	93
94	94
95	95
96	96
97	97
98	98
99	99
100	100



Gigabyte Technology

Title				CPU LGA1150-A			
Size	Document Number						Rev
Custom	GA-H81M-DS2						3.0
Date: Tuesday, August 19, 2014				Sheet 4 of 29			

LGA1150 (A)

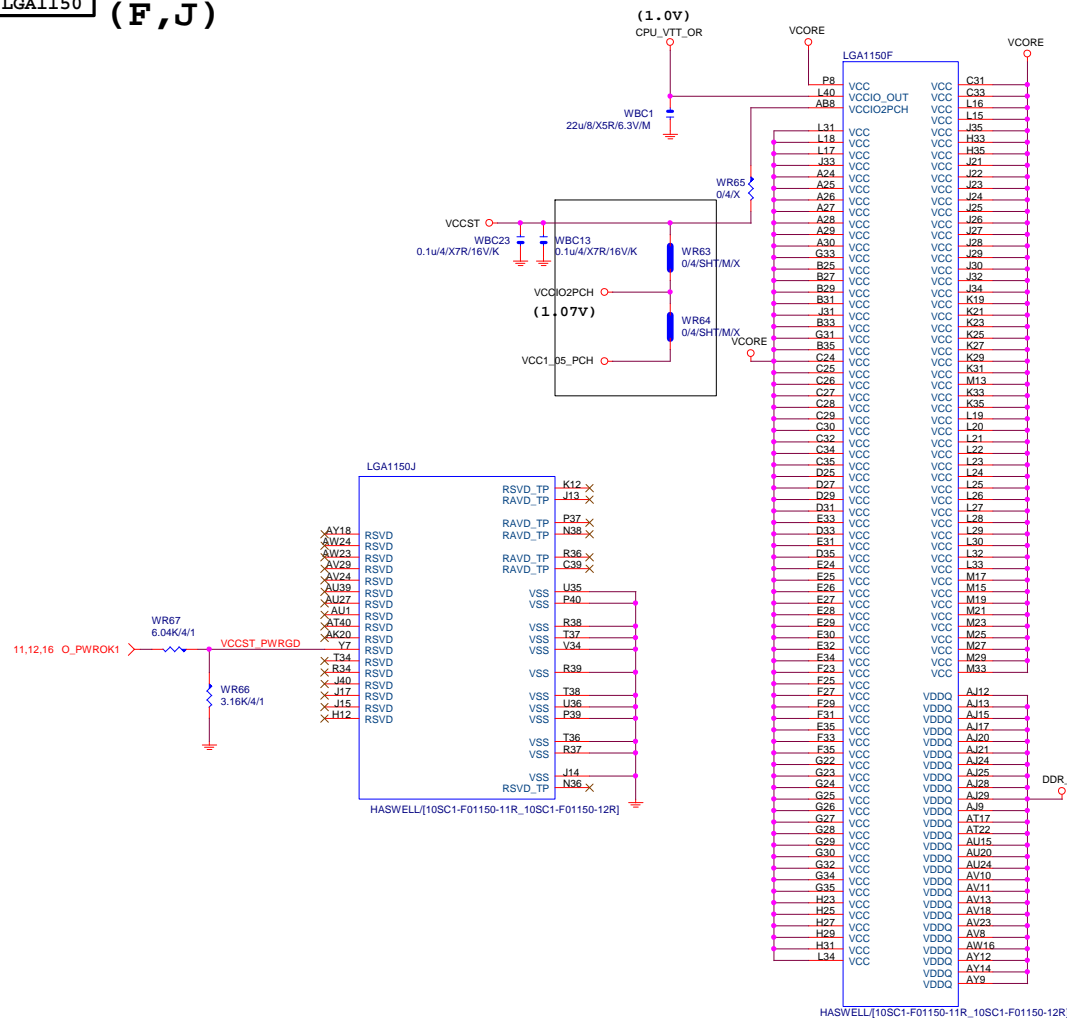
LGA1150A									
MAAA0	AU13	DDR0_MA0	DDR0_D00	AD38	MDA0				
MAAA1	AV16	DDR0_MA1	DDR0_D01	AD39	MDA1				
MAAA2	AU16	DDR0_MA2	DDR0_D02	AF38	MDA2				
MAAA3	AW17	DDR0_MA3	DDR0_D03	AF39	MDA3				
MAAA4	AU17	DDR0_MA4	DDR0_D04	AD37	MDA4				
MAAA5	AW18	DDR0_MA5	DDR0_D05	AD40	MDA5				
MAAA6	AV17	DDR0_MA6	DDR0_D06	AE37	MDA6				
MAAA7	AT18	DDR0_MA7	DDR0_D07	AF40	MDA7				
MAAA8	AU18	DDR0_MA8	DDR0_D08	AH40	MDA9				
MAAA9	AT19	DDR0_MA9	DDR0_D09	AH39	MDA10				
MAAA10	AW11	DDR0_MA10	DDR0_D10	AK38	MDA10				
MAAA11	AV19	DDR0_MA11	DDR0_D11	AK39	MDA11				
MAAA12	AU19	DDR0_MA12	DDR0_D12	AH37	MDA12				
MAAA13	AY10	DDR0_MA13	DDR0_D13	AH38	MDA13				
MAAA14	AT20	DDR0_MA14	DDR0_D14	AK37	MDA14				
MAAA15	AU21	DDR0_MA15	DDR0_D15	AK40	MDA15				
MODT_A0	AW10	DDR0_ODT0	DDR0_D16	AM40	MDA17				
MODT_A1	AY8	DDR0_ODT1	DDR0_D17	AM39	MDA21				
AW8		DDR0_ODT2	DDR0_D18	AP38	MDA18				
AW8		DDR0_ODT3	DDR0_D19	AP39	MDA19				
AW33			DDR0_D20	AM37	MDA20				
AW33			DDR0_D21	AM38	MDA16				
AU31			DDR0_D22	AP37	MDA22				
AU31			DDR0_D23	AP40	MDA23				
AT33			DDR0_D24	AW37	MDA29				
AT33			DDR0_D25	AU35	MDA26				
AT33			DDR0_D26	AU35	MDA27				
AT31			DDR0_D27	T37	MDA28				
AW31			DDR0_D28	AU37	MDA24				
AW31			DDR0_D29	AT35	MDA30				
			DDR0_D30	AW35	MDA31				
			DDR0_D31	AY6	MDA33				
			DDR0_D32	AU6	MDA37				
			DDR0_D33	AW6	MDA36				
			DDR0_D34	AW4	MDA38				
			DDR0_D35	AW4	MDA39				
			DDR0_D36	AR1	MDA41				
			DDR0_D37	AR4	MDA45				
			DDR0_D38	AN3	MDA42				
			DDR0_D39	AN4	MDA43				
			DDR0_D40	AR2	MDA44				
			DDR0_D41	AR3	MDA40				
			DDR0_D42	AN2	MDA46				
			DDR0_D43	AN1	MDA47				
			DDR0_D44	AL1	MDA49				
			DDR0_D45	AL4	MDA53				
			DDR0_D46	AL4	MDA50				
			DDR0_D47	AJ4	MDA51				
			DDR0_D48	AL2	MDA52				
			DDR0_D49	AJ2	MDA48				
			DDR0_D50	AJ2	MDA54				
			DDR0_D51	AJ1	MDA55				
			DDR0_D52	AG1	MDA57				
			DDR0_D53	AG4	MDA61				
			DDR0_D54	AE3	MDA58				
			DDR0_D55	E4	MDA59				
			DDR0_D56	AG2	MDA60				
			DDR0_D57	AG3	MDA56				
			DDR0_D58	AE2	MDA62				
			DDR0_D59	AE1	MDA63				
			DDR0_D60	AE39	DQSA0				
			DDR0_D61	AJ39	DQSA1				
			DDR0_D62	AN39	DQSA2				
			DDR0_D63	AV36	DQSA3				
			DDR0_D64	AV5	DQSA4				
			DDR0_D65	AP3	DQSA5				
			DDR0_D66	AK3	DQSA6				
			DDR0_D67	AF3	DQSA7				
			DDR0_D68	AV32					
			DDR0_D69	AE38	-DQSA0				
			DDR0_D70	AJ38	-DQSA1				
			DDR0_D71	AN38	-DQSA2				
			DDR0_D72	AU36	-DQSA3				
			DDR0_D73	AW5	-DQSA4				
			DDR0_D74	AP2	-DQSA5				
			DDR0_D75	AK2	-DQSA6				
			DDR0_D76	AF2	-DQSA7				
			DDR0_D77	AU32					

HASWELL[10SC1-F01150-11R_10SC1-F01150-12R]

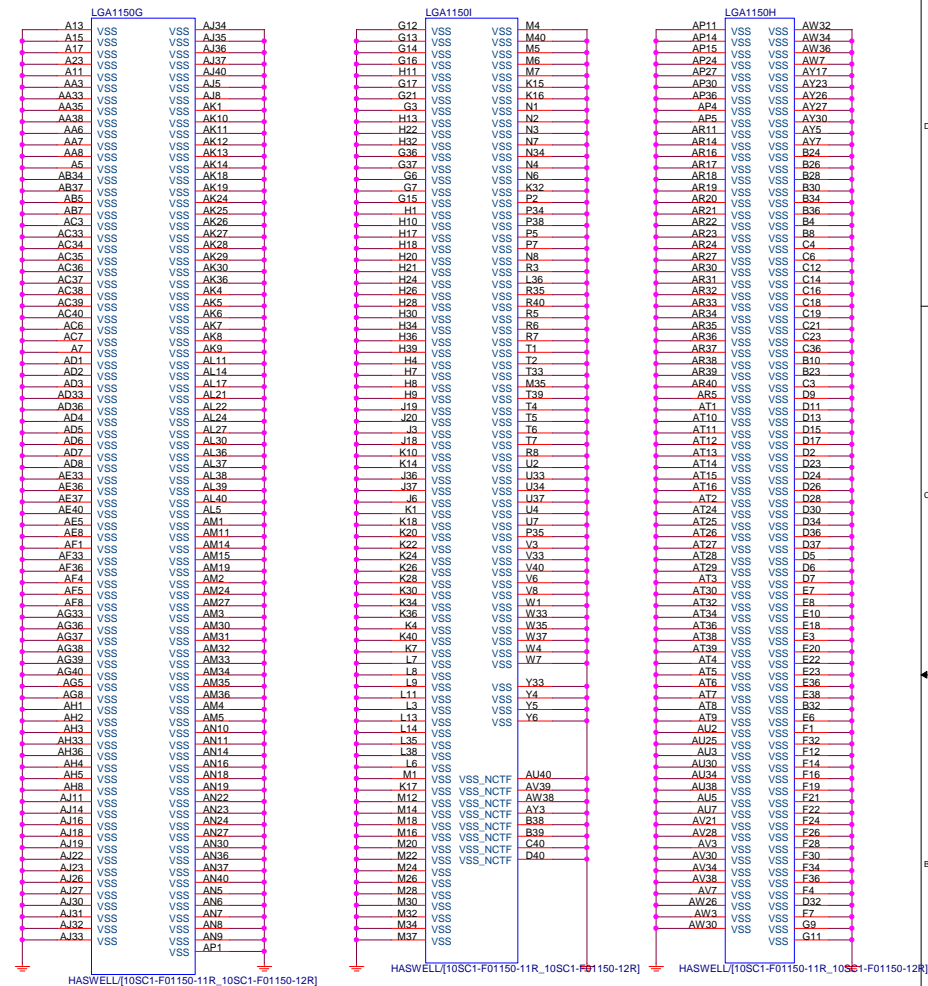
LGA1150 (B)

LGA1150B									
	MAAB0	AL19	DDR1_MA0	DDR1_D00	AE34	MD80			
	MAAB1	AK23	DDR1_MA1	DDR1_D01	AE35	MD81			
	MAAB2	AM22	DDR1_MA2	DDR1_D02	AG35	MD82			
	MAAB3	AM23	DDR1_MA3	DDR1_D03	AH35	MD83			
	MAAB4	AP23	DDR1_MA4	DDR1_D04	AD34	MD84			
	MAAB5	AL23	DDR1_MA5	DDR1_D05	AD35	MD85			
	MAAB6	AY24	DDR1_MA6	DDR1_D06	AG34	MD86			
	MAAB7	AV25	DDR1_MA7	DDR1_D07	AH34	MD87			
	MAAB8	AU26	DDR1_MA8	DDR1_D08	AL34	MD88			
	MAAB9	AW25	DDR1_MA9	DDR1_D08	AL35	MD89			
	MAAB10	AP18	DDR1_MA10	DDR1_D010	AK31	MD810			
	MAAB11	AY26	DDR1_MA11	DDR1_D011	AL31	MD811			
	MAAB12	AV26	DDR1_MA12	DDR1_D012	AK34	MD812			
	MAAB13	AR15	DDR1_MA13	DDR1_D013	AK35	MD813			
	MAAB14	AV27	DDR1_MA14	DDR1_D014	AK32	MD814			
	MAAB15	AY28	DDR1_MA15	DDR1_D015	AL32	MD815			
	MODT_B0	AM17	DDR1_ODT0	DDR1_D016	AN34	MD816			
	MODT_B1	AL16	DDR1_ODT1	DDR1_D017	AP34	MD817			
		AM16	DDR1_ODT2	DDR1_D018	AN31	MD819			
		AK15	DDR1_ODT3	DDR1_D020	AP31	MD823			
				DDR1_D021	AN35	MD820			
		AM26	DDR1_ECC0	DDR1_D022	AP35	MD816			
		AM25	DDR1_ECC1	DDR1_D023	AN32	MD818			
		AP25	DDR1_ECC2	DDR1_D024	AP32	MD822			
		AP26	DDR1_ECC3	DDR1_D025	AM29	MD825			
		AL26	DDR1_ECC4	DDR1_D026	AM28	MD828			
		AL25	DDR1_ECC5	DDR1_D027	AR29	MD827			
		AR26	DDR1_ECC6	DDR1_D028	AR28	MD830			
		AR25	DDR1_ECC7	DDR1_D029	AL28	MD834			
				DDR1_D030	AP29	MD826			
		AK17	DDR1_BA0	DDR1_D031	AP28	MD831			
		AL18	DDR1_BA1	DDR1_D032	AR12	MD832			
		AW28	DDR1_BA2	DDR1_D033	AL13	MD834			
				DDR1_D034	AL12	MD835			
		AW29	DDR1_CKE0	DDR1_D035	AR13	MD836			
		AY29	DDR1_CKE1	DDR1_D036	AP13	MD837			
		AU28	DDR1_CKE2	DDR1_D037	AM13	MD838			
		AU29	DDR1_CKE3	DDR1_D038	AM12	MD839			
				DDR1_D039	AR9	MD845			
		AP17	DDR1_CS_N0	DDR1_D040	AP9	MD841			
		AN15	DDR1_CS_N1	DDR1_D041	AR6	MD847			
		AN17	DDR1_CS_N2	DDR1_D042	AP6	MD843			
		AL15	DDR1_CS_N3	DDR1_D043	AR10	MD844			
				DDR1_D044	AP10	MD840			
				DDR1_D045	AR7	MD846			
				DDR1_D046	AP7	MD842			
				DDR1_D047	AM9	MD852			
				DDR1_D048	AL9	MD853			
				DDR1_D049	AL6	MD850			
				DDR1_D050	AL7	MD855			
				DDR1_D051	AM10	MD848			
				DDR1_D052	AL10	MD849			
				DDR1_D053	AM6	MD854			
				DDR1_D054	AM7	MD851			
				DDR1_D055	AH6	MD861			
				DDR1_D056	AH7	MD860			
				DDR1_D057	AH6	MD859			
				DDR1_D058	AE7	MD863			
				DDR1_D059	AJ6	MD866			
				DDR1_D061	AJ7	MD857			
				DDR1_D062	AF6	MD858			
				DDR1_D063	AF7	MD862			
				DDR1_D064	AF35	MD860			
				DDR1_D0S_P0	AL33	DSB81			

LGA1150 (F, J)

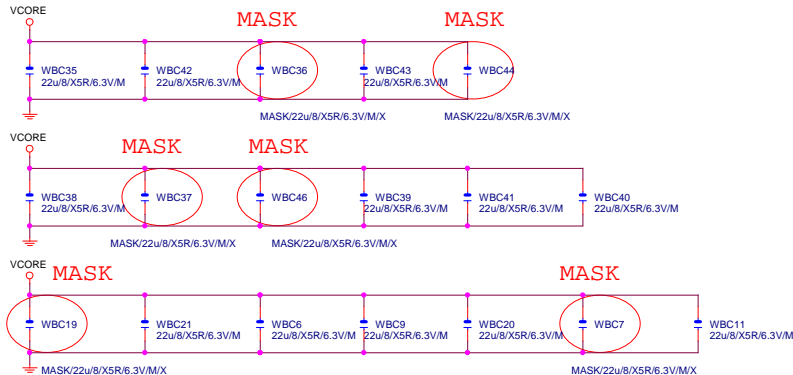


LGA1155 (G,H,I)



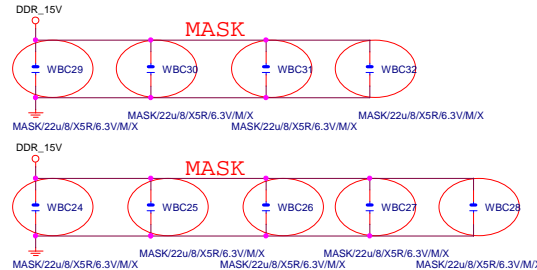
VCore CAP

(x18)



DDR CAP

(x9)



Gigabyte Technology

Title	CPU I GA1150-C
-------	----------------

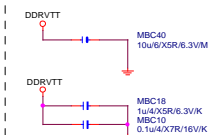
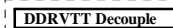
Size	Document Number
Custom	GA-H81M-DS2

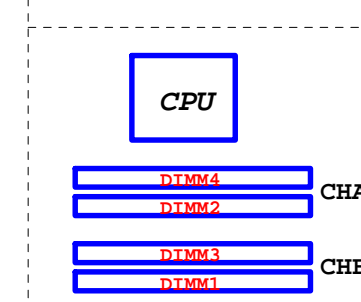
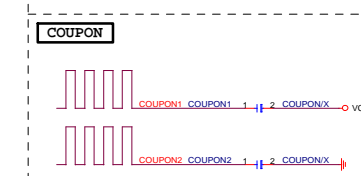
Custom **CA-1101M-DSZ** 3.0

Date: Tuesday, August 19, 2014 Sheet 6 of 29

Rev
3.0

(A)





DMI:12/4/4/4/12(breakout min 8/4/4/4/8)
Impedance=85 +- 17.5%

VCC1_5_PCH

NR50 7.5K/4/1 DMI_COMP B19
PCIE_COMP C13

NR40 7.5K/4/1

10 CK_SRCCLK_PCH CK_SRCCLK_PCH G22
10 CK_SRCCLK_PCH CK_SRCCLK_PCH F22

CLKIN_DMI_N
CLKIN_DMI_P

□

1

```
PCIEX1:16/5/5/5/16 (breakout min 8/4/4/4/8)
```

PCHJ

PCHB
B85: Port 6/7 N/A
H81: Port 6/7/12/13 N/A

N_USBP0	↔	N_USBP0	17
N_+USBP0	↔	N_+USBP0	17
N_USBP1	↔	N_USBP1	17
N_+USBP1	↔	N_+USBP1	17
N_USBP2	↔	N_+USBP1	23
N_+USBP2	↔	N_USBP2	23
N_USBP3	↔	N_+USBP3	23
N_+USBP3	↔	N_USBP3	23

H81: Port 6/7/12/13 N/A

N-USBP8	N-USBP8	20
N+USBP8	N+USBP8	20
N-USBP9	N-USBP9	20
N+USBP9	N+USBP9	20
N-USBP10	N-USBP10	20
N+USBP10	N+USBP10	20
N-USBP11	N-USBP11	20
N+USBP11	N+USBP11	20

] H81: Port 6/7/12/13 N/A

OC0B_GP59 AE40
 OC1B_GP40 AF37
 OC2B_GP41 AD39
 OC3B_GP42 AD40
 OC4B_GP43 AF39
 OC5B_GP43 AC41
 OC6B_GP10 AF40
 OC7B_GP10 AG40

N_USBOC_R 17
 N_USBOC_F 20

W=4 mil out of PCH
 S=15 mil out of PCB

USB2 PHY

USBRBIAS

USBRBIAS

AV20 N USBRBIAS NR47 22.6/4/1

AU20

_KIN_DOT96N AP11 CK_DOTCLK

_KIN_DOT96P AM11 CK_DOTCLK

NR130
8.2K/4

N_GPIO14

O_3VDUAL

N_USBOC F

N_USBOC R

NBC82
0.1u4/X7R/16V/K

NBC83
0.1u4/X7R/16V/K

21

PCHF	
USB3	FDILINK

N/A [

- ~~K20~~ USB3_RXN_4
- ~~L20~~ USB3_RXP_4
- ~~D15~~ USB3_TXN_4
- ~~C15~~ USB3_TXP_4
- ~~L18~~ USB3_RXN_5
- ~~K18~~ USB3_RXP_5
- ~~B14~~ USB3_TXN_5
- ~~A14~~ USB3_TXP_5

FDI_RCOMP

BD82B81/S/[10HB1-030H81-10R]

FDI_TXP[0..1] >> FDI_TXP[0..1] 4

FDI_TXN[0..1] >> FDI_TXN[0..1] 4

Impedance=85 +/- 17.5%
Back Panel < 10000 MILS
Front Panel < 6000 MILS

Mount for integrated clock Generation Mode



PCHJ

Page 10 of 10

PCH_HS
PCH_HS/[12SP2-030005-51R_12SP2-030005-52R_12SP2-030005-53R_12SP2-030005-54R_12SP2-030005-55R_12SP2-030005-56R_12SP2-030005-57R_12SP2-030005-58R_12SP2-030005-59R_12SP2-030005-60R_12SP2-030005-61R_12SP2-030005-62R_12SP2-030005-63R_12SP2-030005-64R_12SP2-030005-65R_12SP2-030005-66R_12SP2-030005-67R_12SP2-030005-68R_12SP2-030005-69R_12SP2-030005-70R_12SP2-030005-71R_12SP2-030005-72R_12SP2-030005-73R_12SP2-030005-74R_12SP2-030005-75R_12SP2-030005-76R_12SP2-030005-77R_12SP2-030005-78R_12SP2-030005-79R_12SP2-030005-80R_12SP2-030005-81R_12SP2-030005-82R_12SP2-030005-83R_12SP2-030005-84R_12SP2-030005-85R_12SP2-030005-86R_12SP2-030005-87R_12SP2-030005-88R_12SP2-030005-89R_12SP2-030005-90R_12SP2-030005-91R_12SP2-030005-92R_12SP2-030005-93R_12SP2-030005-94R_12SP2-030005-95R_12SP2-030005-96R_12SP2-030005-97R_12SP2-030005-98R_12SP2-030005-99R_12SP2-030005-100R]

OC[7:4]# for Device 26 (ports 8-13)

--	--

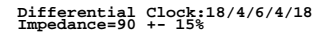
CONCLUSIONS

	1
--	---

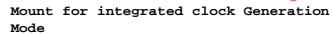
(E)



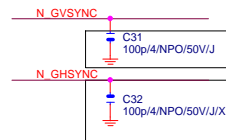
(G)



PCH	CLK	PD
-----	-----	----



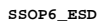
VGA DDC



VGA CONNECTOR



VGA ESD

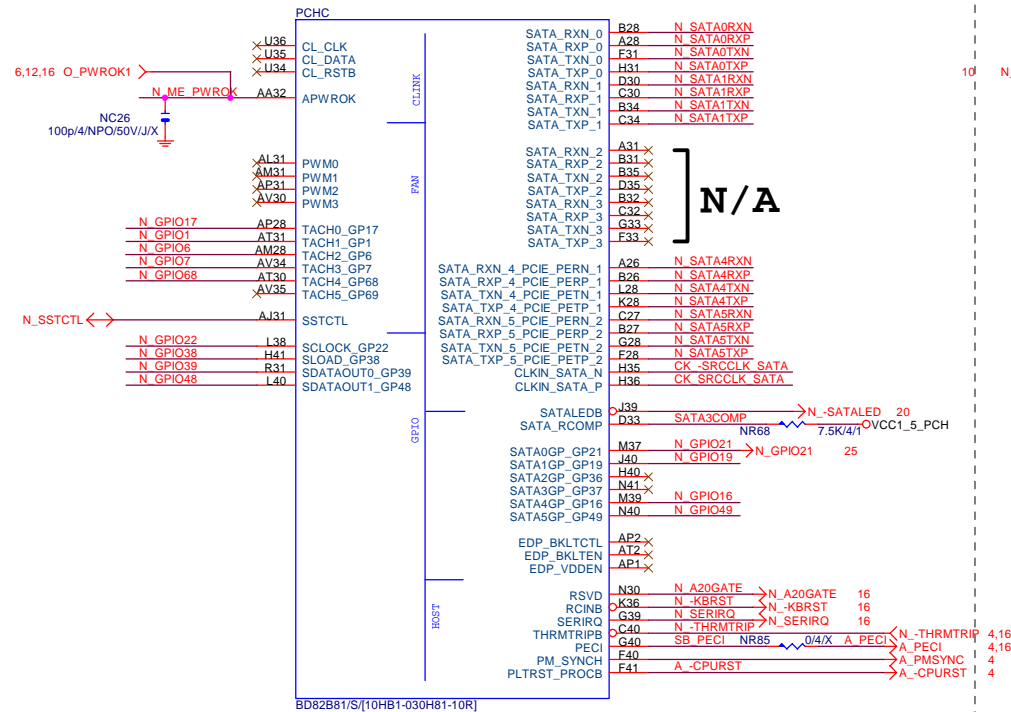


VGA DDC

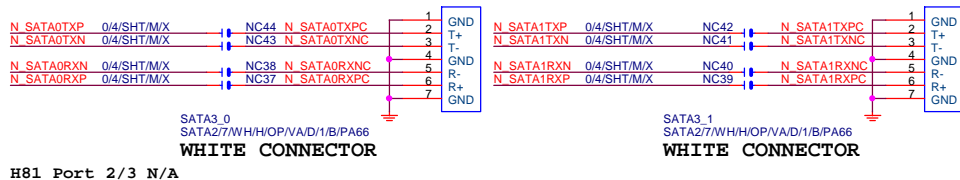


(C)

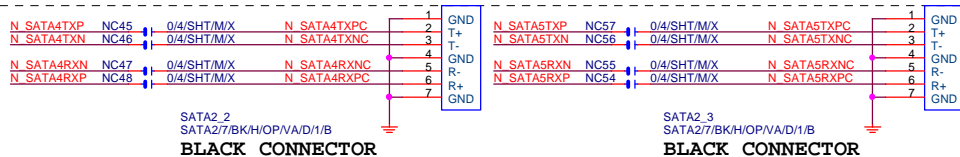
SATA3 : 20/7.5/4.5/7.5/20 (breakout min 8/4/4/4/8)
Impedance=90 +- 17.5%
SATA2 : 15/7.5/4.5/7.5/15 (breakout min 8/4/4/4/8)
Impedance=90 +- 17.5%



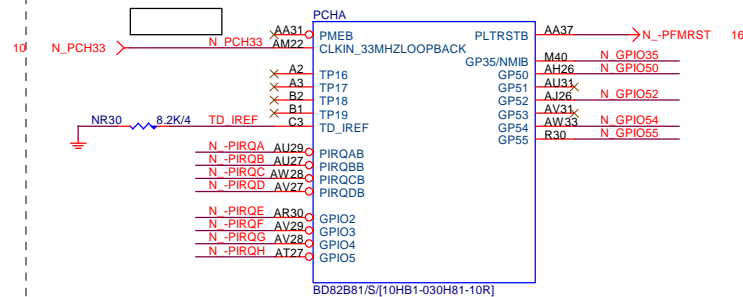
SATA CONNECTOR



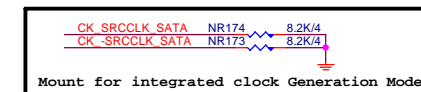
```
** Z87/H87 Port 4&5 SATA3.0
** B85 Port 4&5 SATA2.0
```



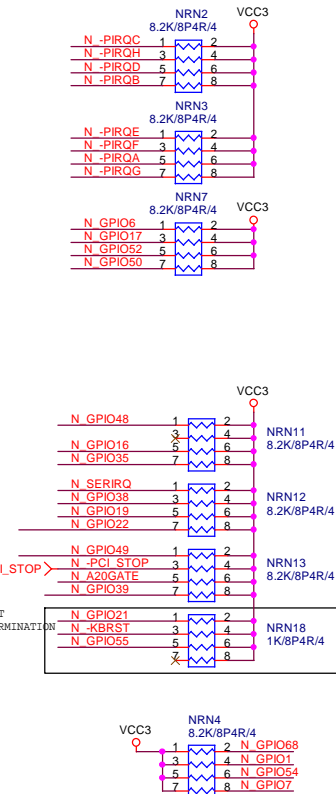
(A)



PCH	CLK	PD
-----	-----	----

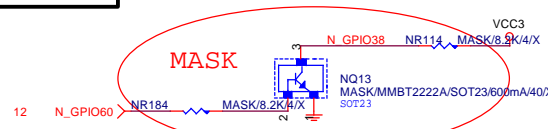


PCH	PU/PD
-----	-------

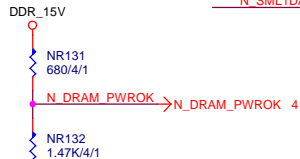


ME PWROK

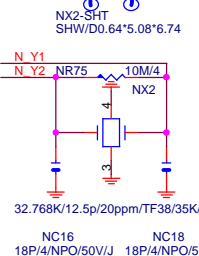
GPIO38 Ctrl



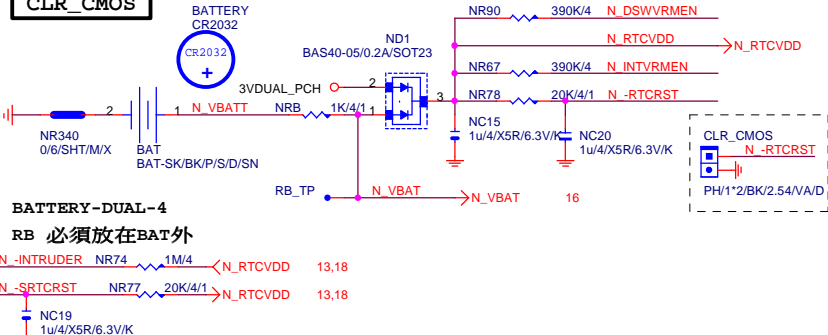
(D)



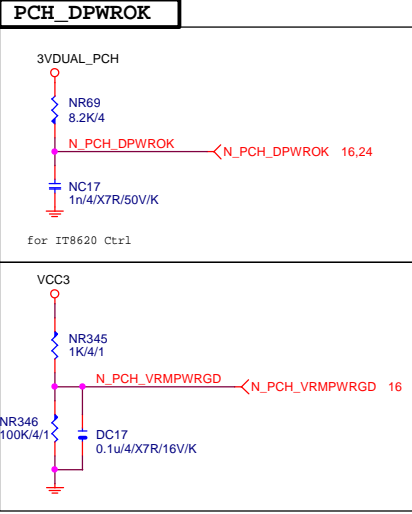
HSW STRAP13



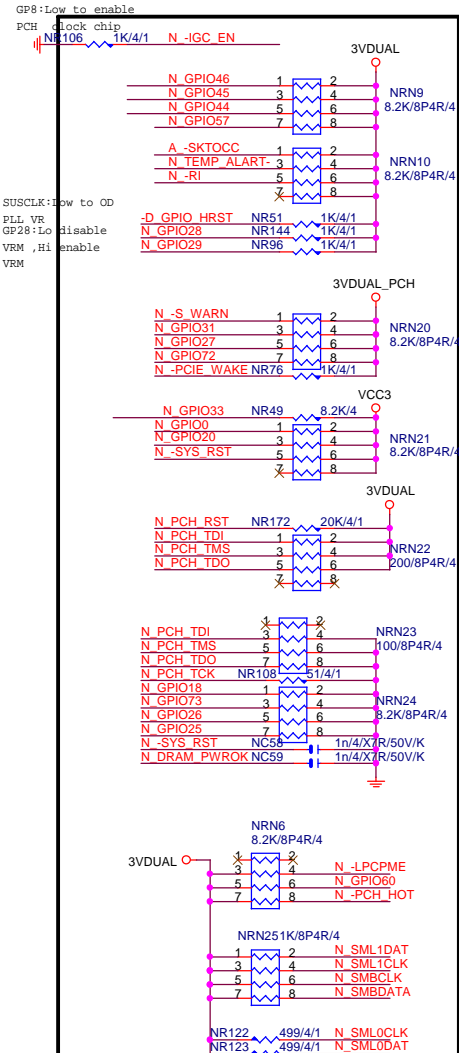
CLR CMOS



ACZ SDOUT

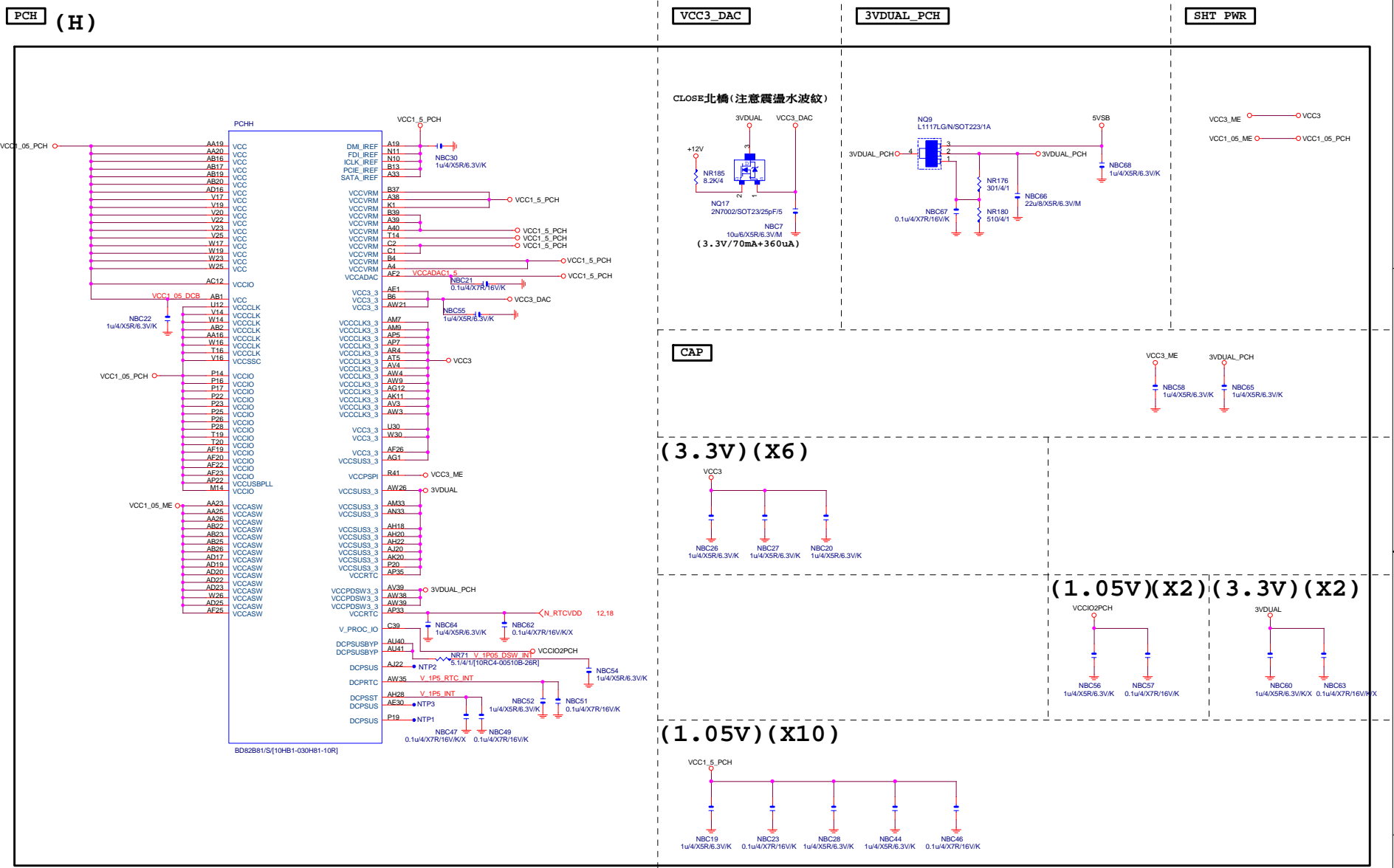


PCH	PU/PD
-----	-------

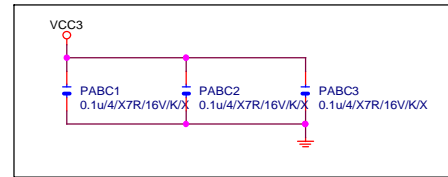


Gigabyte Technology

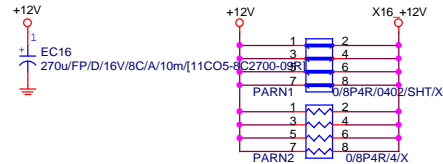
Title			
PCH GPIO, CTRL, AUDIO			
Size	Document Number		Rev
Custom	GA-H81M-DS2		3.0
Date:	Tuesday, August 19, 2014	Sheet	12 of 29



PCIEX16 CAP



PCIEX16 PROTECT SHT

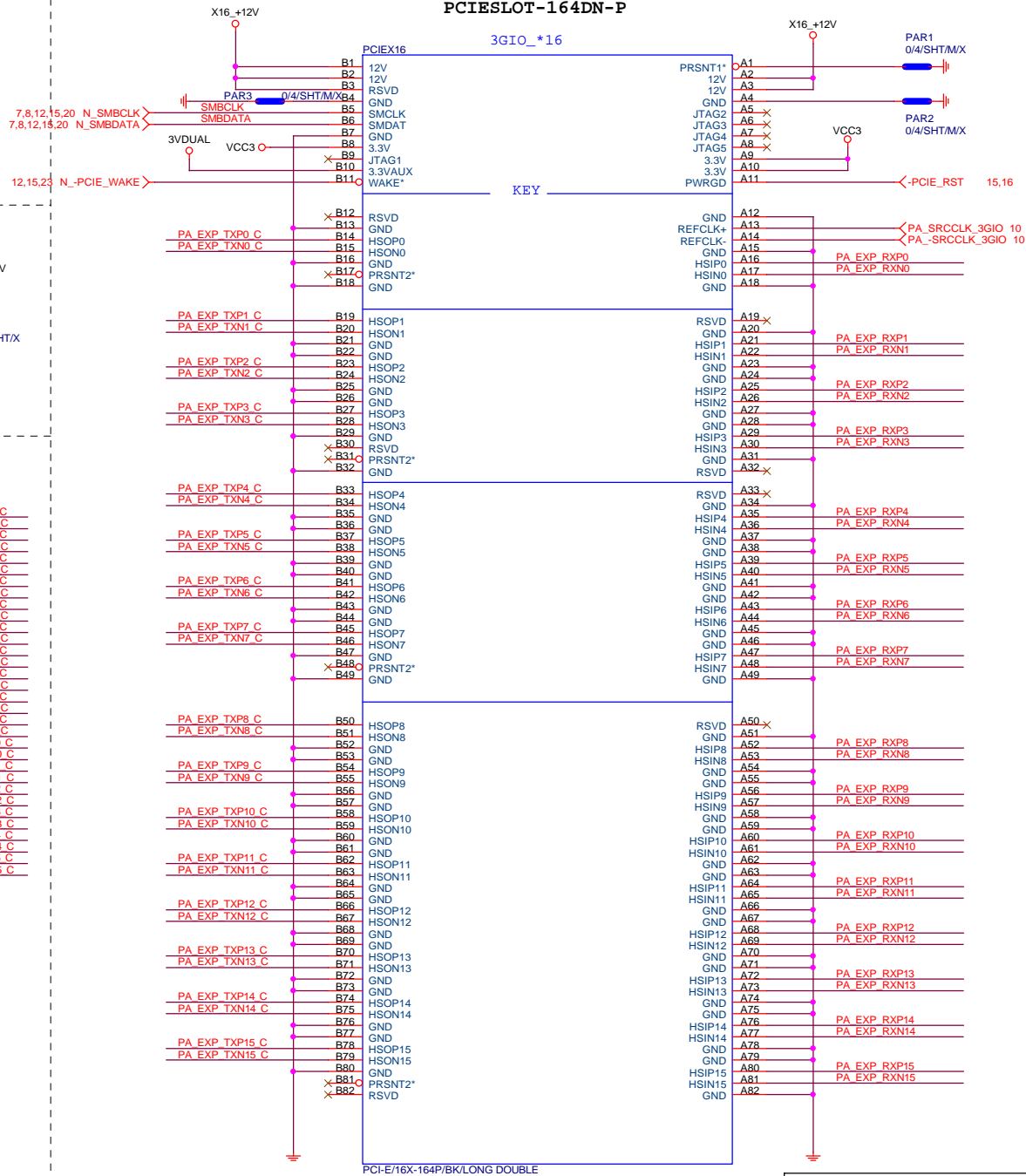


PCIEX16 AC CAP

PA EXP TXP0	PAC5	0.22uF/4X5R/6.3V/K	PA EXP TXP0 C
PA EXP TXN0	PAC4	0.22uF/4X5R/6.3V/K	PA EXP TXN0 C
PA EXP TXP1	PAC6	0.22uF/4X5R/6.3V/K	PA EXP TXP1 C
PA EXP TXN1	PAC7	0.22uF/4X5R/6.3V/K	PA EXP TXN1 C
PA EXP TXP2	PAC8	0.22uF/4X5R/6.3V/K	PA EXP TXP2 C
PA EXP TXN2	PAC9	0.22uF/4X5R/6.3V/K	PA EXP TXN2 C
PA EXP TXP3	PAC10	0.22uF/4X5R/6.3V/K	PA EXP TXP3 C
PA EXP TXN3	PAC11	0.22uF/4X5R/6.3V/K	PA EXP TXN3 C
PA EXP TXP4	PAC12	0.22uF/4X5R/6.3V/K	PA EXP TXP4 C
PA EXP TXN4	PAC13	0.22uF/4X5R/6.3V/K	PA EXP TXN4 C
PA EXP TXP5	PAC14	0.22uF/4X5R/6.3V/K	PA EXP TXP5 C
PA EXP TXN5	PAC15	0.22uF/4X5R/6.3V/K	PA EXP TXN5 C
PA EXP TXP6	PAC16	0.22uF/4X5R/6.3V/K	PA EXP TXP6 C
PA EXP TXN6	PAC17	0.22uF/4X5R/6.3V/K	PA EXP TXN6 C
PA EXP TXP7	PAC19	0.22uF/4X5R/6.3V/K	PA EXP TXP7 C
PA EXP TXN7	PAC18	0.22uF/4X5R/6.3V/K	PA EXP TXN7 C
PA EXP TXP8	PAC20	0.22uF/4X5R/6.3V/K	PA EXP TXP8 C
PA EXP TXN8	PAC21	0.22uF/4X5R/6.3V/K	PA EXP TXN8 C
PA EXP TXP9	PAC22	0.22uF/4X5R/6.3V/K	PA EXP TXP9 C
PA EXP TXN9	PAC23	0.22uF/4X5R/6.3V/K	PA EXP TXN9 C
PA EXP TXP10	PAC24	0.22uF/4X5R/6.3V/K	PA EXP TXP10 C
PA EXP TXN10	PAC25	0.22uF/4X5R/6.3V/K	PA EXP TXN10 C
PA EXP TXP11	PAC26	0.22uF/4X5R/6.3V/K	PA EXP TXP11 C
PA EXP TXN11	PAC27	0.22uF/4X5R/6.3V/K	PA EXP TXN11 C
PA EXP TXP12	PAC28	0.22uF/4X5R/6.3V/K	PA EXP TXP12 C
PA EXP TXN12	PAC29	0.22uF/4X5R/6.3V/K	PA EXP TXN12 C
PA EXP TXP13	PAC30	0.22uF/4X5R/6.3V/K	PA EXP TXP13 C
PA EXP TXN13	PAC31	0.22uF/4X5R/6.3V/K	PA EXP TXN13 C
PA EXP TXP14	PAC32	0.22uF/4X5R/6.3V/K	PA EXP TXP14 C
PA EXP TXN14	PAC33	0.22uF/4X5R/6.3V/K	PA EXP TXN14 C
PA EXP TXP15	PAC34	0.22uF/4X5R/6.3V/K	PA EXP TXP15 C
PA EXP TXN15	PAC35	0.22uF/4X5R/6.3V/K	PA EXP TXN15 C

PA EXP RXIP0.[15] >> PA_EXP_RXIP[0..15] 4
PA EXP RXN0.[15] >> PA_EXP_RXN[0..15] 4
PA EXP TXIP0.[15] >> PA_EXP_TXIP[0..15] 4
PA EXP TXN0.[15] >> PA_EXP_TXN[0..15] 4

PCIEX16 SLOT



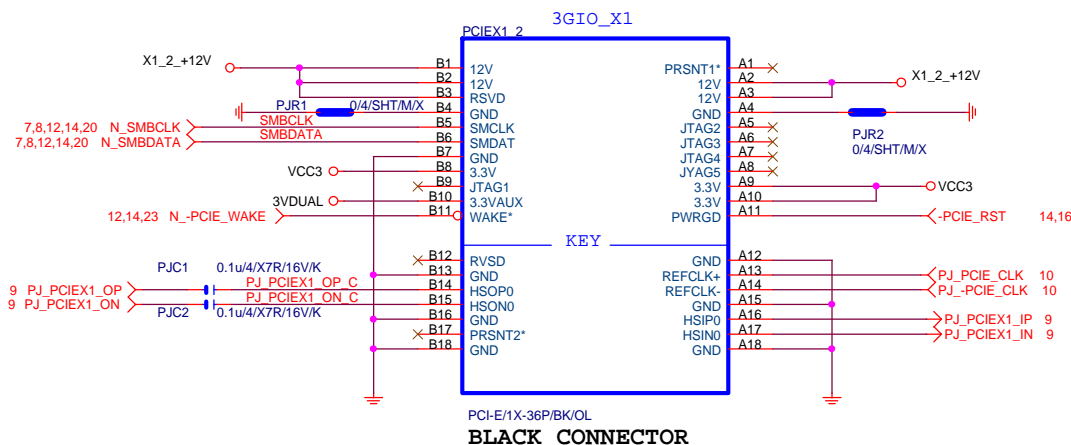
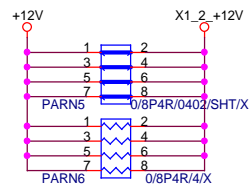
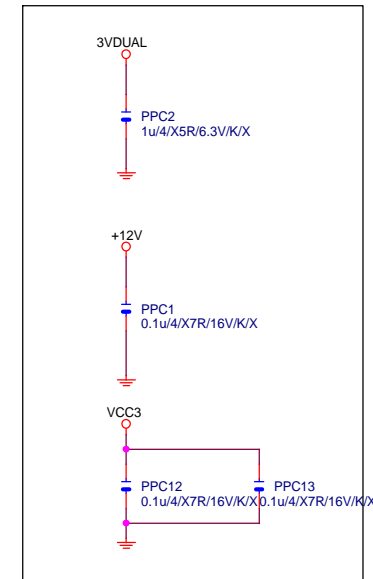
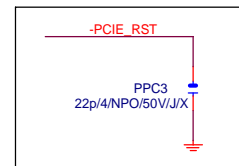
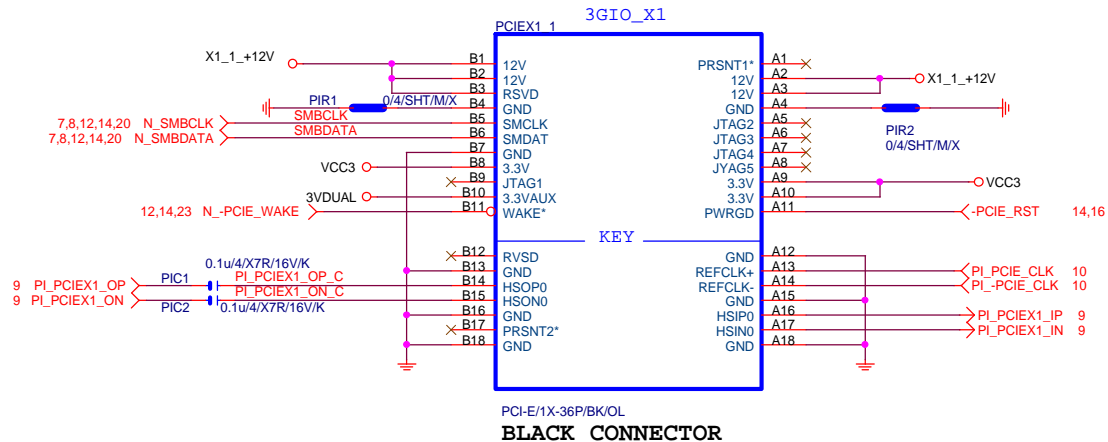
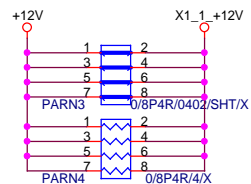
BLACK CONNECTOR

Gigabyte Technology

Title			PCI EXPRESS * 16		
Size			Document Number		
Custom			GA-H81M-DS2		
Date:			Rev		
Tuesday, August 19, 2014			3.0		
Sheet			14 of 29		

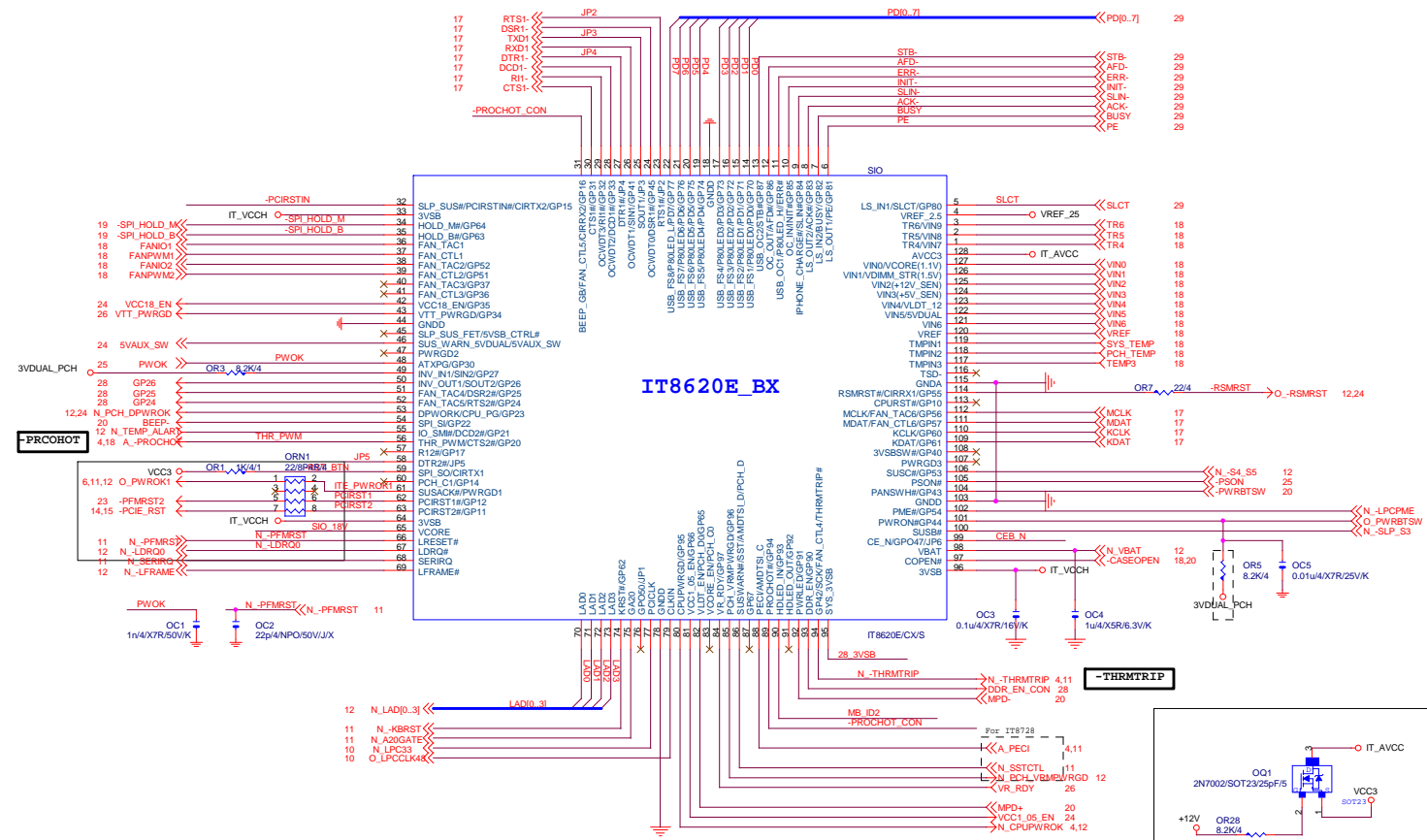
PCIEX1 SLOT

PCIEX1 PROTECT SHT



Gigabyte Technology			
PCI EXPRESS X 1 PORT			
Title	Document Number		
Size	GA-H81M-DS2		
Custom	Rev 3.0		
Date:	Tuesday, August 19, 2014	Sheet 15	of 29

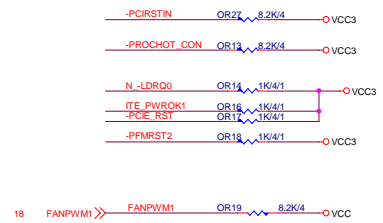
SIO IT8620



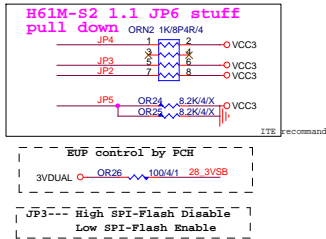
PWR SHT



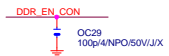
SIO PU



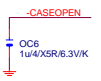
SIO STRAP



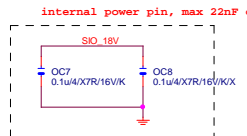
Power leakage



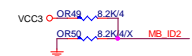
DUAL BIOS OPT STRAP



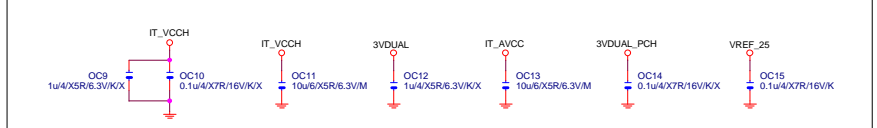
SIO_18V

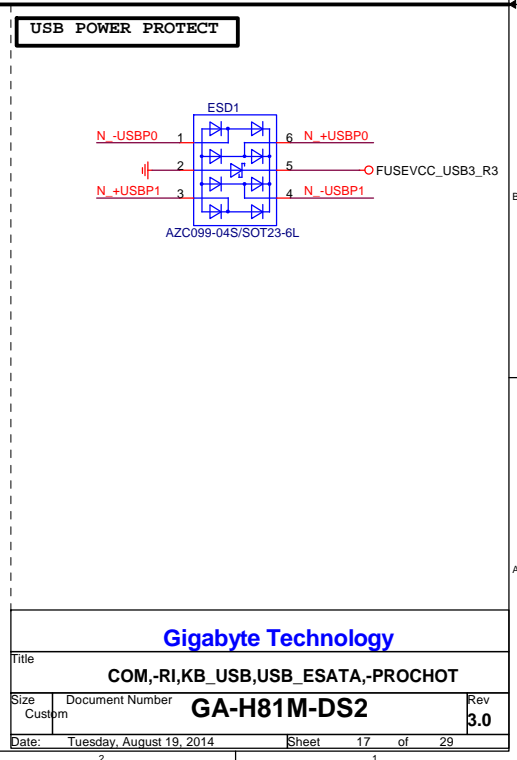
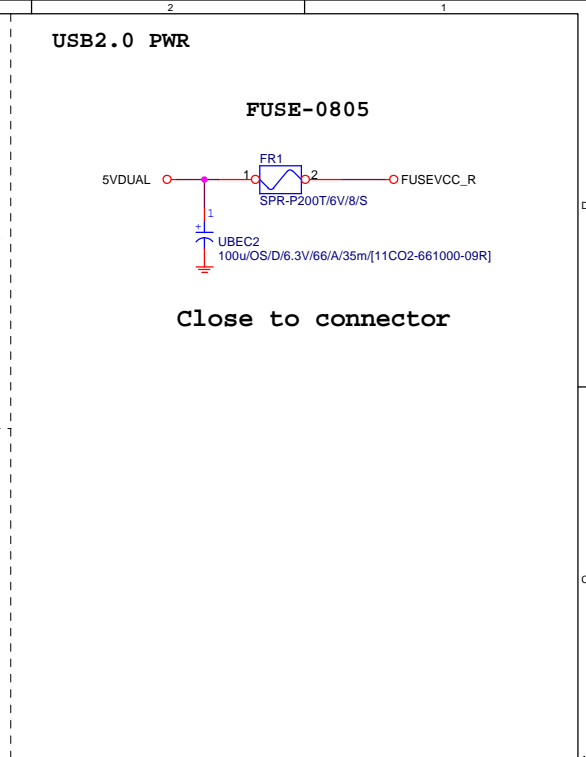


MB ID

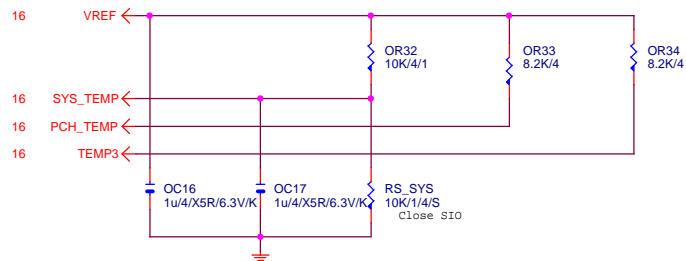


SIO CAP

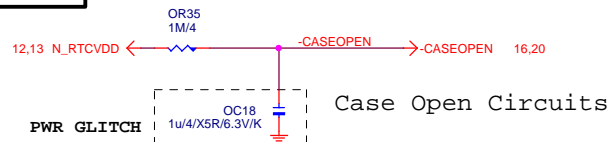




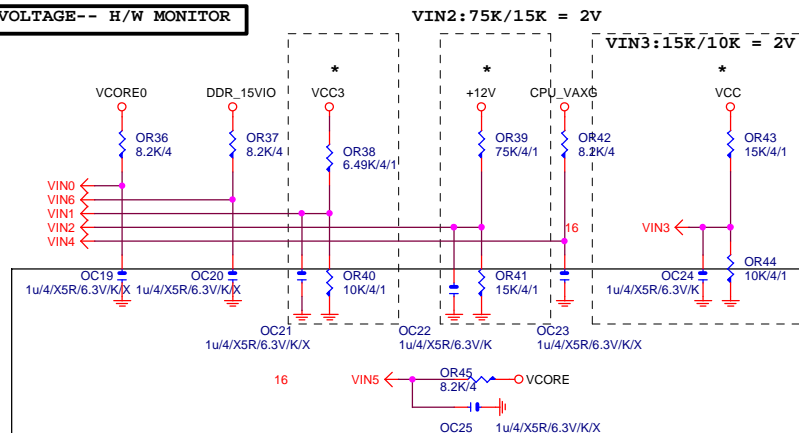
TEMP H/W MONITOR



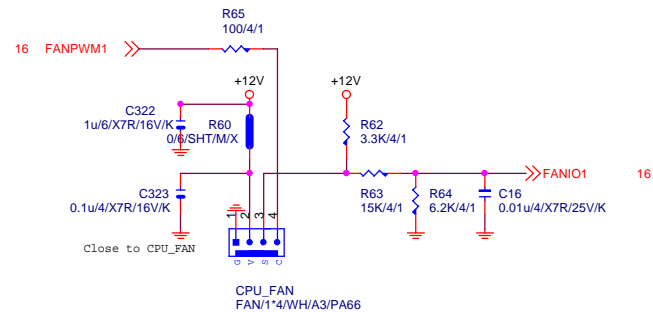
CASE OPEN



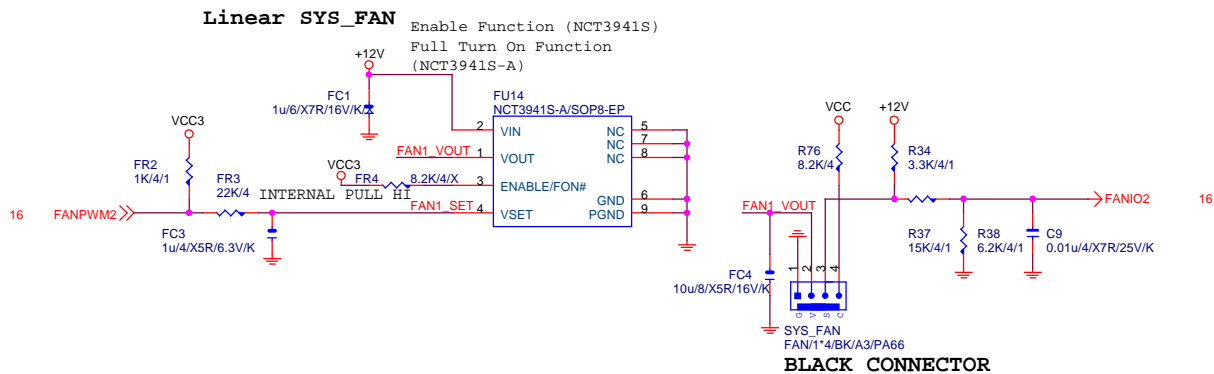
VOLTAGE-- H/W MONITOR



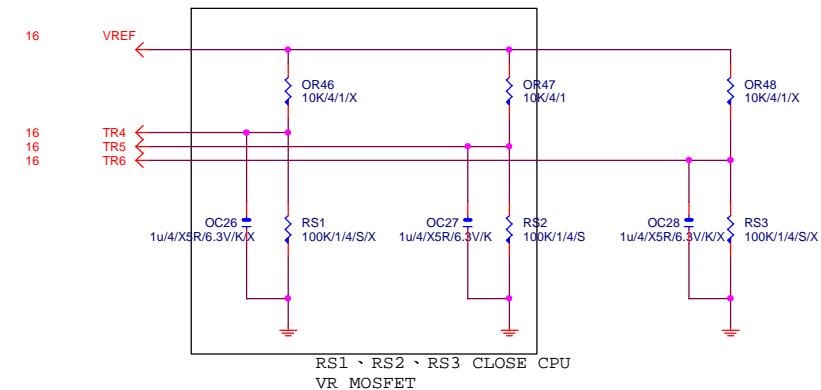
CPU SMART FAN

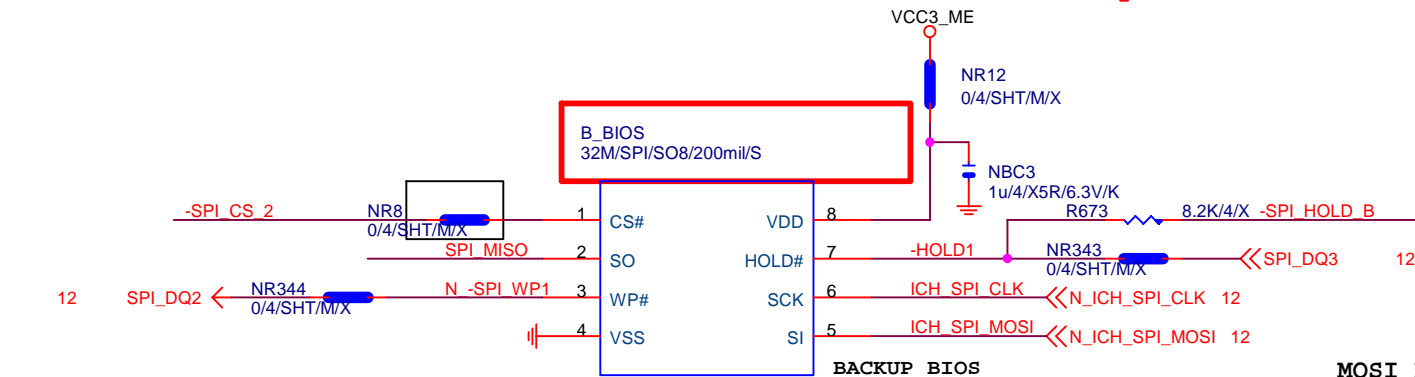
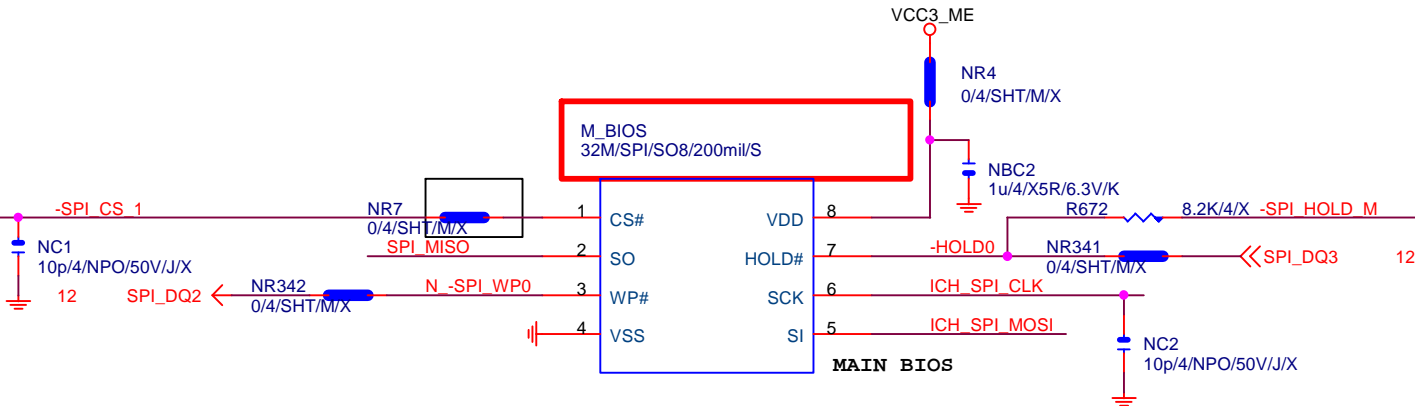


SYS SMART FAN



-PROHOT

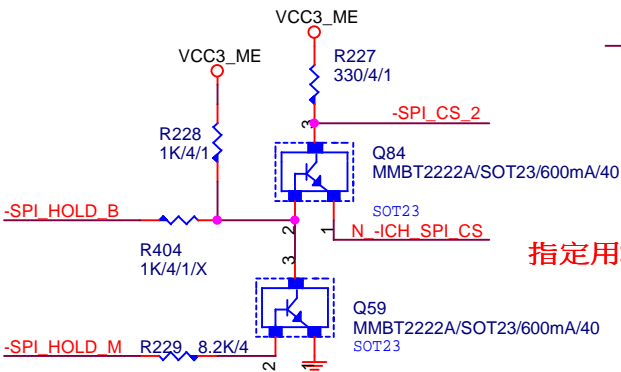
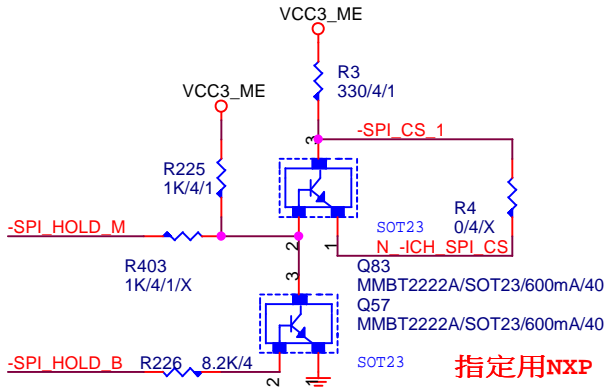
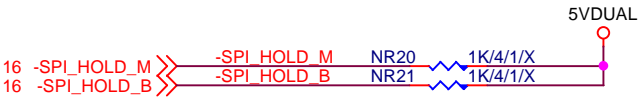
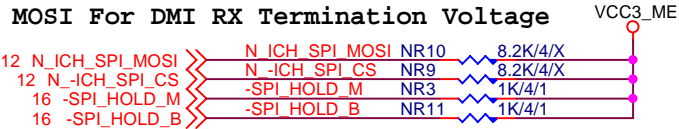




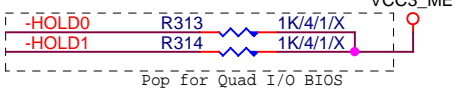
BOOT DEVICE	GNT0	GNT1
LPC	0	0
PCI	0	1
NAND	1	0
SPI	1	1

1 means floating
0 means PD 1K

MOSI For DMI RX Termination Voltage



CHECK



Gigabyte Technology

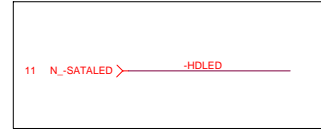
DUAL BIOS

GA-H81M-DS2

Rev 3.0

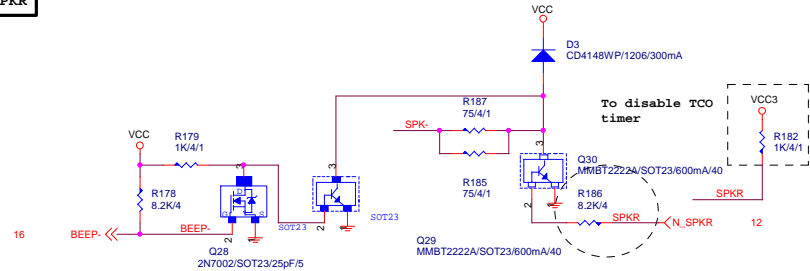
Title		Date: Tuesday, August 19, 2014		Sheet 19 of 29
Size Custom	Document Number	GA-H81M-DS2		Rev 3.0

SATA LED

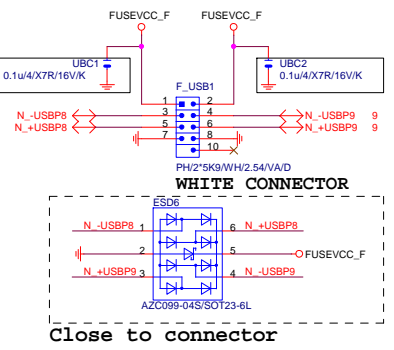


-USBOC_F

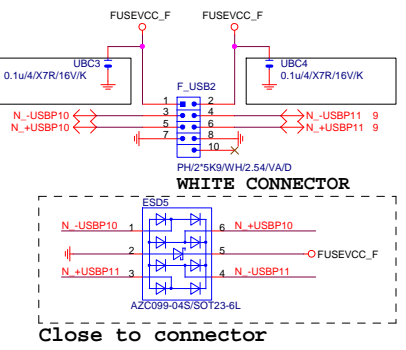
SPKR



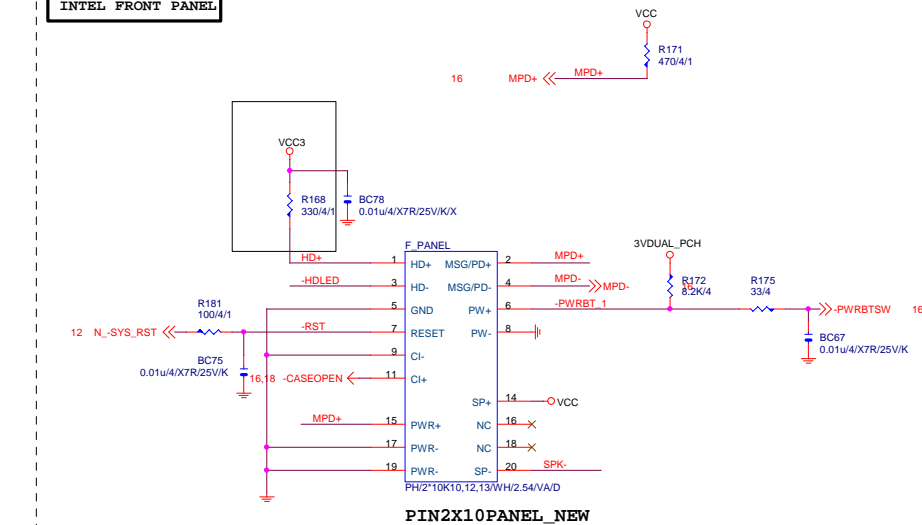
FRONT USB1



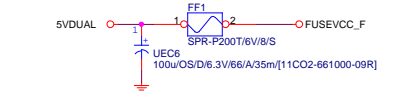
FRONT USB2



INTEL FRONT PANEL



FUSE-0805

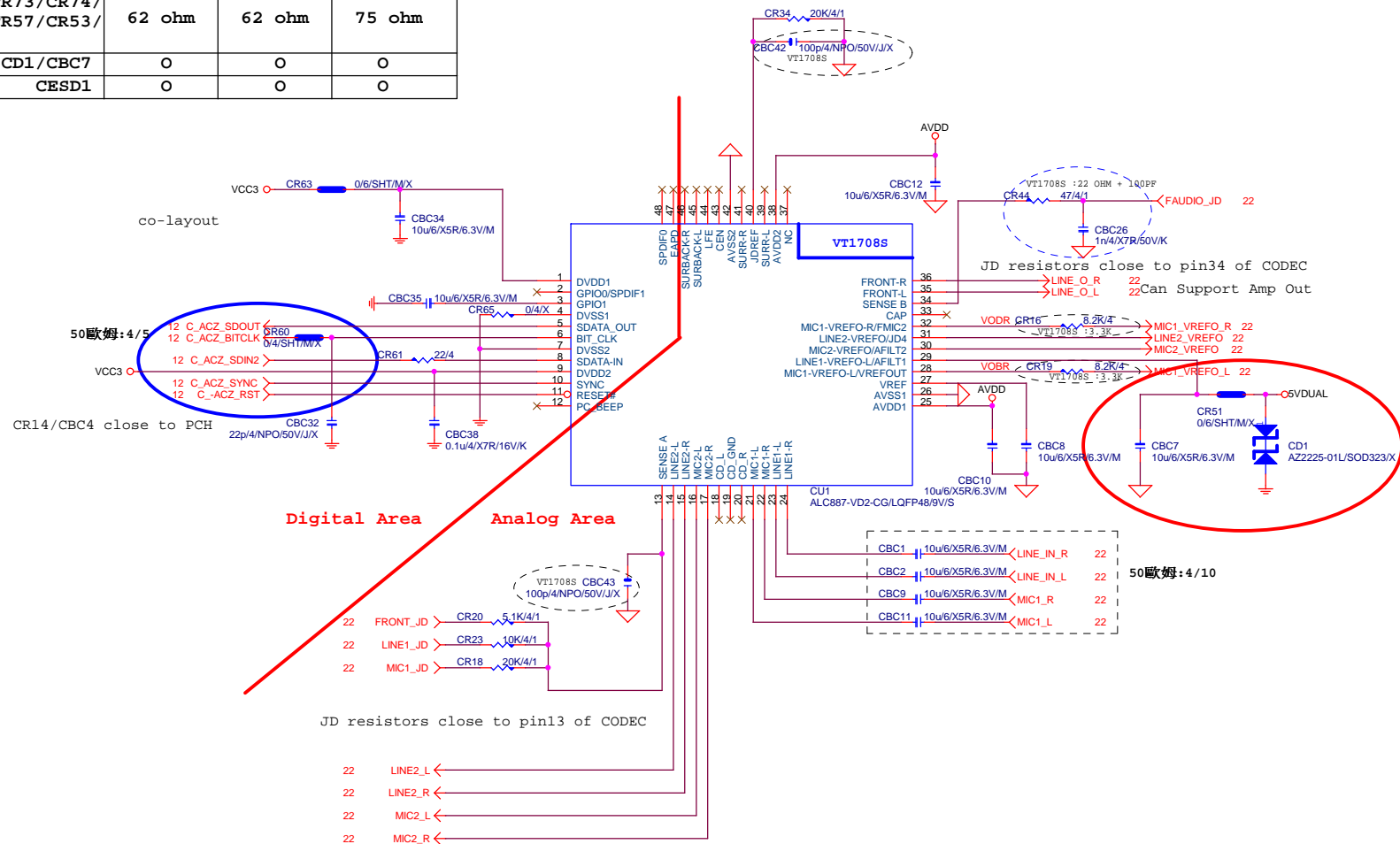


F_USB1, F_USB2 4-Port 2.6A

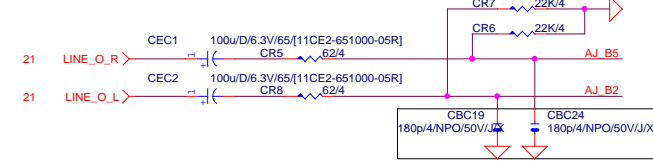
Gigabyte Technology			
FP,F_USB,USB PWR,SPKR,SATA LED			
Size	Document Number	Rev	
Custom		GA-H81M-DS2	
Date:	Tuesday, August 19, 2014	Sheet	20 of 29

AZALIA CODEC ALC892/ALC887-VD2/VT1708-CE Colay

	ALC892	ALC887-VD2	VT1708S-CE
CR44/CBC26	47ohm+1nF	47ohm+1nF	22ohm+100P
CBC42/CBC43	X	X	100P/4
CR6/CR7/CR58/CR54/ CR67/CR68/CR69/CR70	22K/4	22K/4	10K/4/1
CR5/CR8/CR1/CR14/ CR17/CR22/CR73/CR74/ CR13/CR11/CR57/CR53/ CR75/CR76	62 ohm	62 ohm	75 ohm
CR51/CD1/CBC7	O	O	O
CESD1	O	O	O

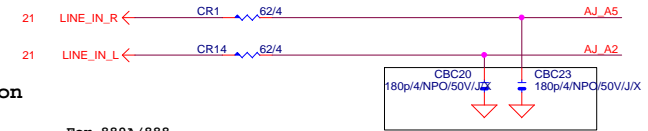


LINE-OUT



LINE-IN

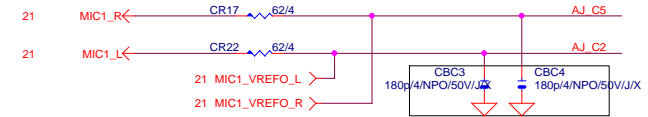
Only reserved for ALC888



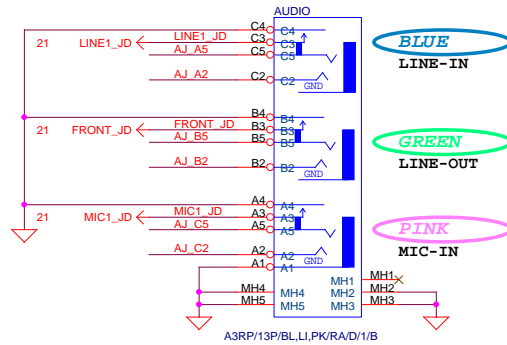
Verify MIC function
in LINE-in

For 889A/888
- - - - -

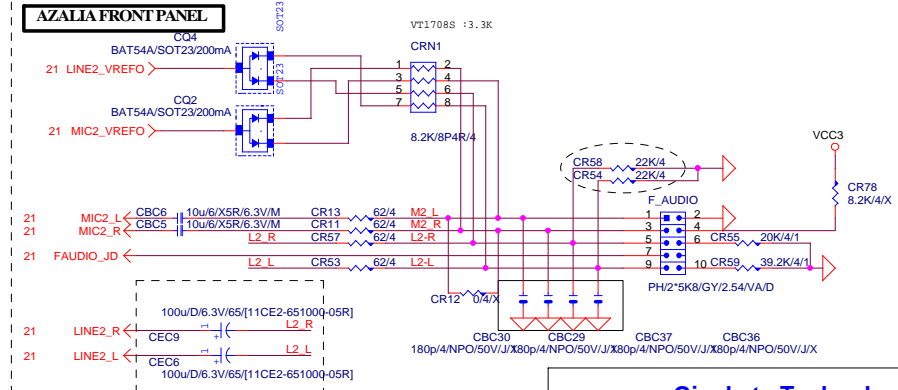
MIC-IN



SPDIF_OUT

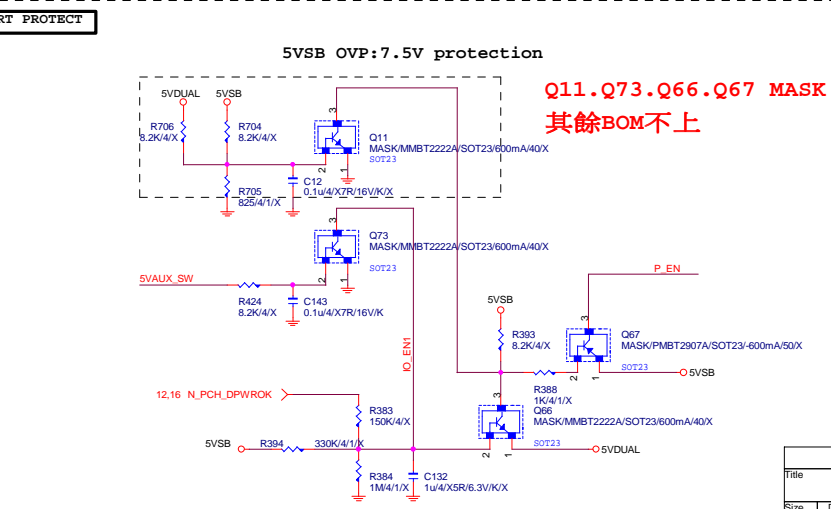
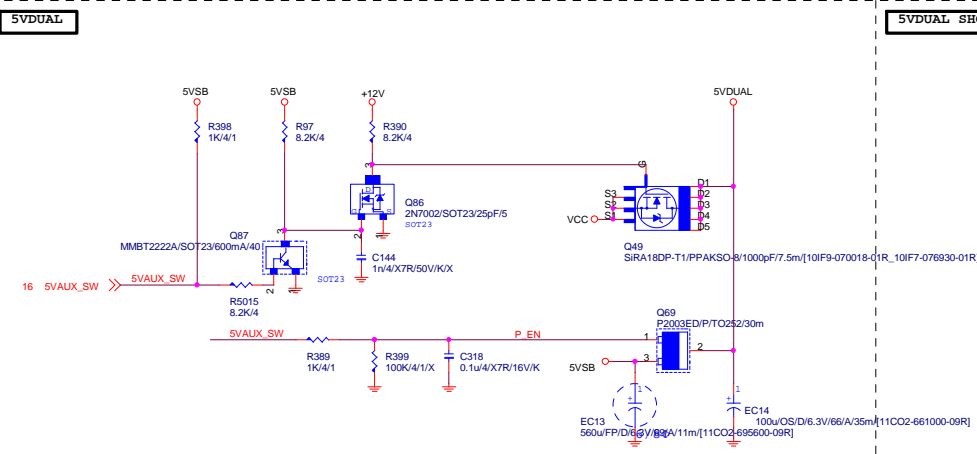
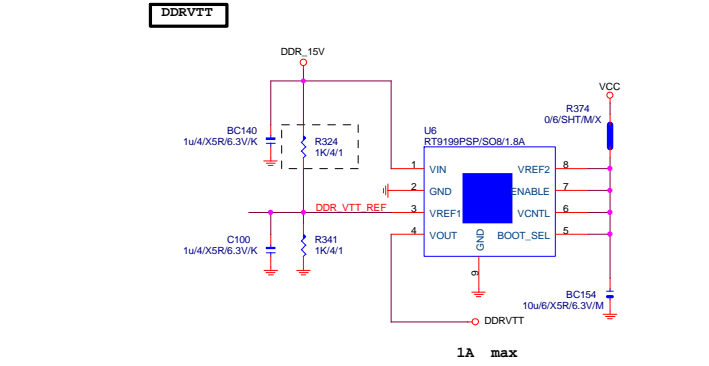
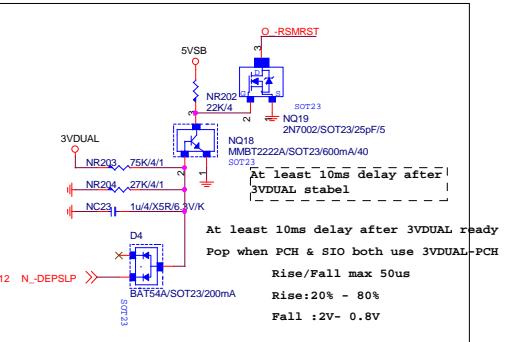
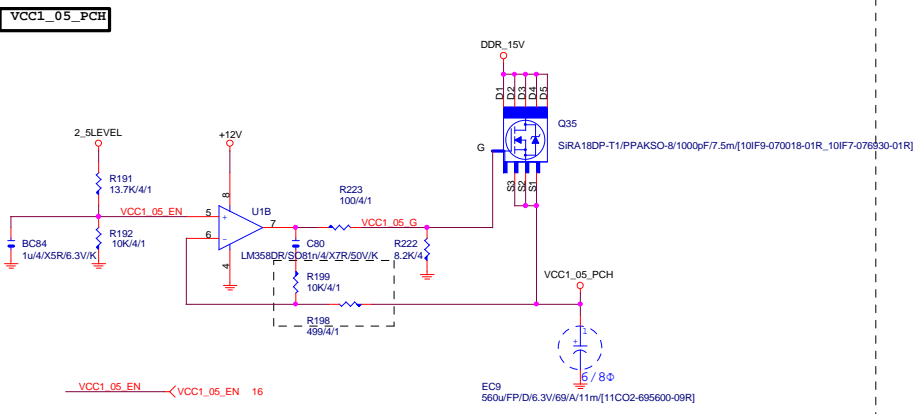
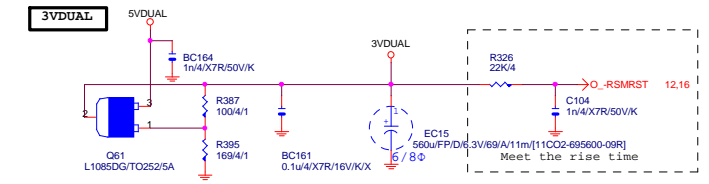
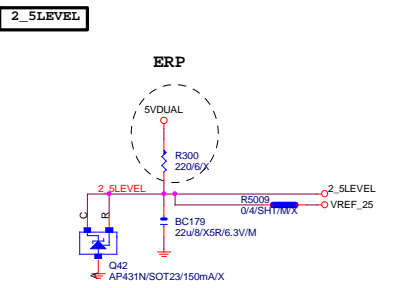
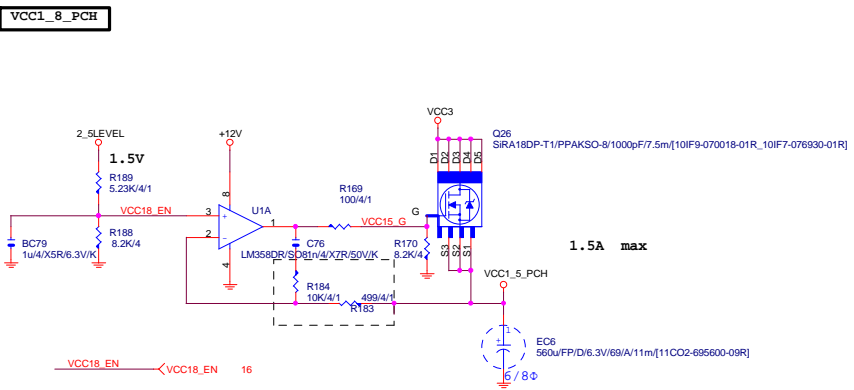


AZALIA FRONT PANEL

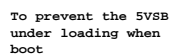
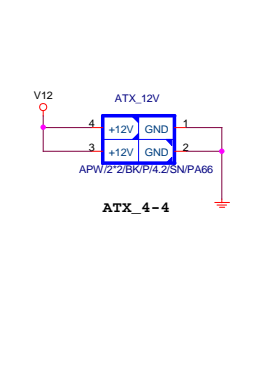
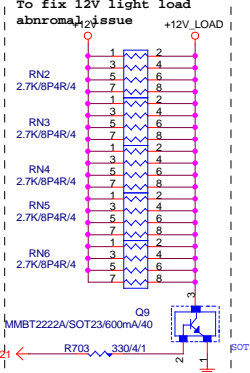
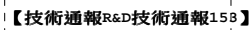


Gigabyte Technology

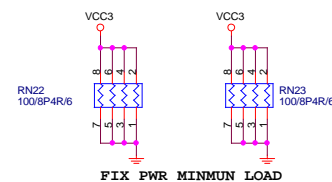
Title			
AUDIO JACK			
Size Custom	Document Number	GA-H81M-DS2	Rev 3.0
Date:	Tuesday, August 19, 2014	Sheet 22 of 29	



【技術通報R&D技術通報155】



【技術通報R&D技術通報154】

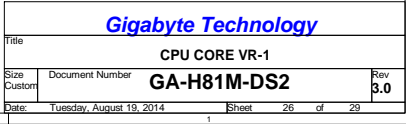


Gigabyte Technology

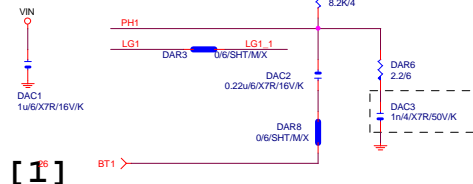
ATX CONNECTOR

GA-H81M-DS2

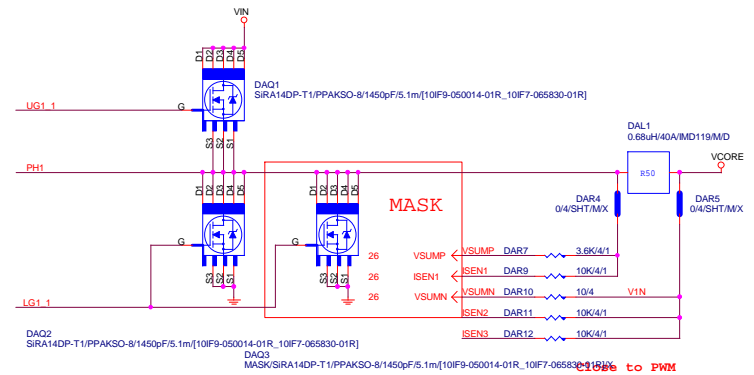
Rev	3.0
-----	-----



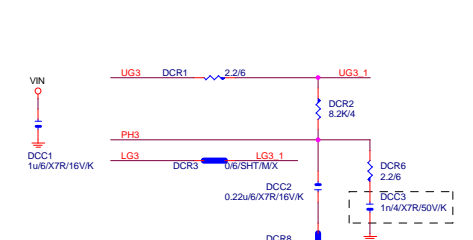
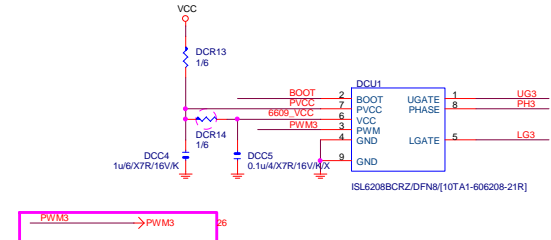
PHASE 1



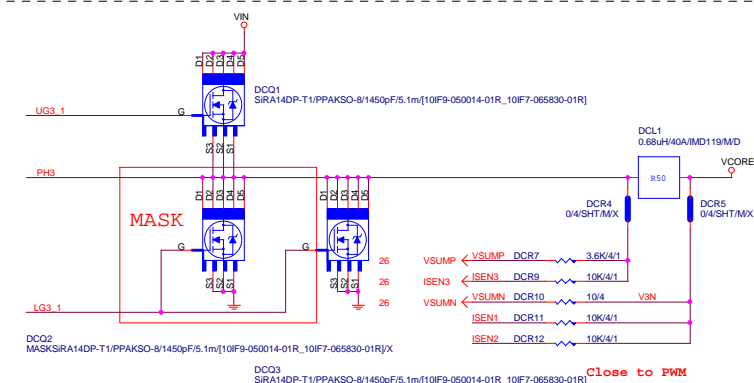
[1]



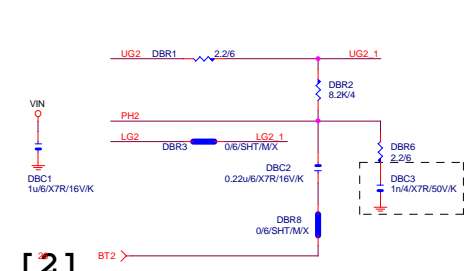
PHASE 3



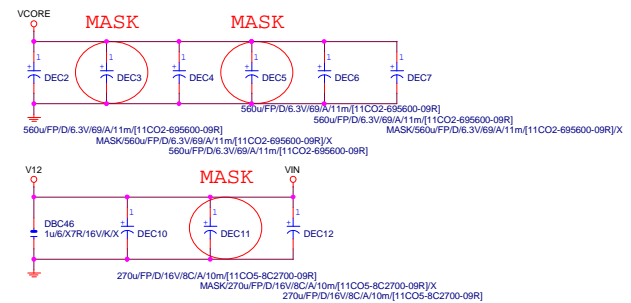
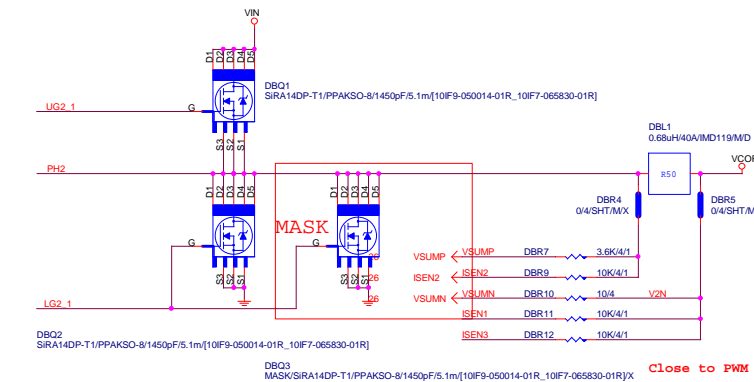
[3]



PHASE 2

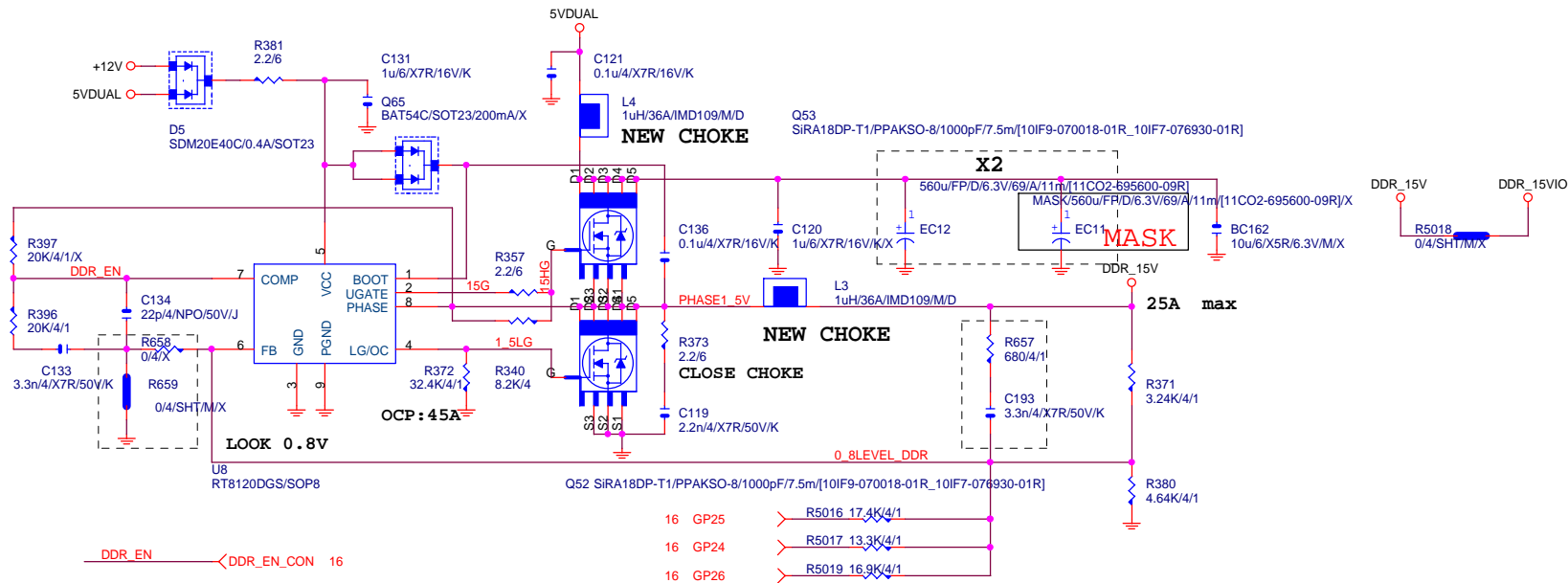


[2]




Gigabyte Technology			
Title		CPU CORE VR-2	
Size	Document Number	GA-H81M-DS2	Rev 3.0
Custom			
Date	Tuesday, August 19, 2014	Sheet	27 of 29

DDR15V



VIN=5V, VOUT=1.5V, IOUT=25A, PHASE=1
IRMS=11.45A
560u/FP/D/6.3V/68/8m RIPPLE CURRENT=4.7A
Coefficient=1.7(85°C), 1(105°C)
VIN Ripple current=4.7X1.7=7.99A(85°C)
-->故固態電容須2X7.99=15.98>11.45A

$$\begin{aligned} \text{Rocset} &= (\text{Iocp} * \text{Lgate}, \text{rdson}) / \text{Iocset} \\ \text{Rocset} &= (45\text{A} * 6.7\text{mOhm}) / 10\text{uA} = 30\text{K} \\ \text{Iocset} &= 10\text{uA} \end{aligned}$$

<div style="text-align: center;">  </div>			
Title			
DDR POWER			
Size Custom	Document Number	GA-H81M-DS2	Rev 3.0
Date:	Tuesday, August 19, 2014	Sheet 28 of 29	

